In the Specification

Please amend the specification of this application as follows:

Rewrite the paragraph at page 1, lines 20 to 22 as follows:

--Serial Number ~~09/438,237~~ 09/483,237, entitled "EMULATION SYSTEM WITH SEARCH AND IDENTIFICATION OF OPTIONAL EMULATION PERIPHERALS" claiming priority from U.S. Provisional Application No. 60/120,960 filed February 19, 1999, now U.S. Patent No. 6,671,665;--

Rewrite the paragraph at page 11, lines 3 to 16 as follows:

--Figure 4 illustrates an electrical connection view of the coupling between access adapter 2 and target system 3. Figure 4 shows the connections of the various signals of the JTAG header 5 illustrated in Figure 2. All these signals are connected to scan controller 41. The signals nTRST, TCK and TMS are connected to two example megamodules 45 and 47. Figure 4 illustrates the optional connection of TCKO to the target system clock SYSCLK. The scan input TDI connects to a scan input of megamodule 45. The scan output of megamodule 45 supplies the scan input of megamodule 47. The scan output of megamodule 47 supplies the scan output TDO. The two extension signals nET0 and nET1 control megamodules 45 and 47 via merge unit ~~32~~ 46. These extension signals are monitored by test equipment 43.--

Rewrite the paragraph at page 16, lines 8 to 19 as follows:

--The interrupt during suspend state 103 transits to the execute state ~~102~~ 101 upon execution of an abort interrupt (ABORTI) instruction. The abort interrupt instruction would ordinarily be used only on detection of an unrecoverable error in the interrupt service routine. The path back to the debug suspend state is broken upon execution of the abort interrupt instruction. The

status of the interrupt during debug state bit and the debug frame counter are ignored in this case. In particular, the interrupt during debug state bit is cleared and the debug frame counter is set to zero. This mechanism enables recovery to the background task when a high priority interrupt service routine has an unrecoverable error.--

Rewrite the paragraph at page 23, lines 10 to 21 as follows:

Rewrite the paragraph at page 23, lines 10 to 21 as follows:
--The data comparison unit 320 configures for event generation where the DMSK register serves as a mask register and the DREF register serves as a comparison reference. The data comparison unit 320 generates a debug suspend request ~~when the DCNTL register DSTOP and DFEN bits are TRUE~~. The DMSK field defines the data comparison unit 320 debug suspend request rudeness level. Generation of an event without generating a debug suspend request allows the data comparison unit 320 event to be used as a trigger generator through the nET0 and nET1 pins without altering core execution. This function supports break and watchpoints, execution pause, and event counting.--